

ABSTRACT OF THE DISCLOSURE

SIGNAL OVERRIDE FOR SIMULATION MODELS

5 A method, system, and data structure for overriding
a signal during model simulation. An override signal
port is instantiated within a model for delivering an
override signal from an instrumentation entity to a
signal selection means, wherein the signal selection
10 means selects between the signal and the override signal.
A signal override is declared during model simulation,
and in response to the declared signal override, the
override signal is selected utilizing the signal
selection means.